

ABSTRACT OF THE DISCLOSURE

A fault management system for a switching equipment is disclosed by which, when an recoverable fault occurs with a processor or a circuit section of a switching equipment, a malfunction of an associated terminal equipment or the like or a fault of a physical layer in an associated apparatus can be detected without depending upon a manual operation of an operator. When a clock fault detection section detects that supply of a clock signal of a quartz oscillator which supplies the clock signal to a processor is interrupted, it notifies a concentrated fault management section of the result of the detection as an unrecoverable fault. The concentrated fault management section receives the notification and continuously signals reset signals for resetting the processor and a circuit section to the processor and the circuit section, respectively. Further, the concentrated fault management section sends a fault notification to a central control section connected to an external console.

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